

**Amendment and Response**

Applicant: Rao Rajashekhar

Serial No.: 10/574,117

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Docket No.: I438.113.101/IF02P041WOUS

Title: DEVICE FOR UTILIZATION WITH THE SYNCHRONIZATION OF CLOCK SIGNALS, AND CLOCK SIGNAL SYNCHRONIZING METHOD

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**IN THE SPECIFICATION**

Please replace the Abstract with the following rewritten paragraph:

**Abstract**

~~A clock signal synchronizing method and an apparatus device for utilization with the synchronization of clock signals is disclosed. In one embodiment the apparatus includes a delay device with a variably controllable delay time into which a clock signal, or a signal obtained therefrom, is input, charged with the variably controllable delay time, and output as a delayed clock signal.~~

~~A device is provided for determining whether a clock edge of the delayed clock signal output by the delay device, or of a signal obtained therefrom, lies within a predetermined time window before a corresponding clock edge of the clock signal, or of the signal obtained therefrom.~~

A clock signal synchronizing method and an apparatus device for utilization with the synchronization of clock signals is disclosed. In one embodiment the apparatus includes a delay device with a variably controllable delay time into which a clock signal, or a signal obtained therefrom, is input, charged with the variably controllable delay time, and output as a delayed clock signal. A device is provided for determining whether a clock edge of the delayed clock signal output by the delay device, or of a signal obtained therefrom, lies within a predetermined time window before a corresponding clock edge of the clock signal, or of the signal obtained therefrom.